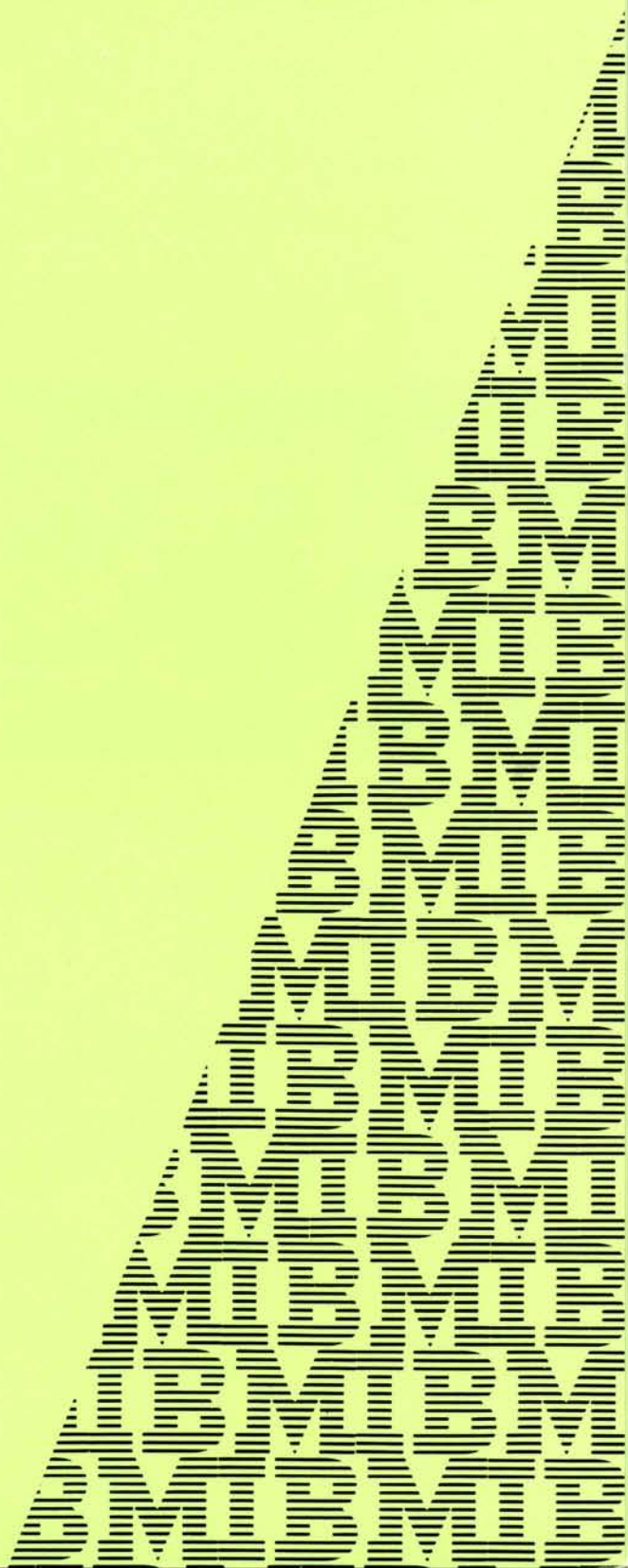


3084 Processor Complex



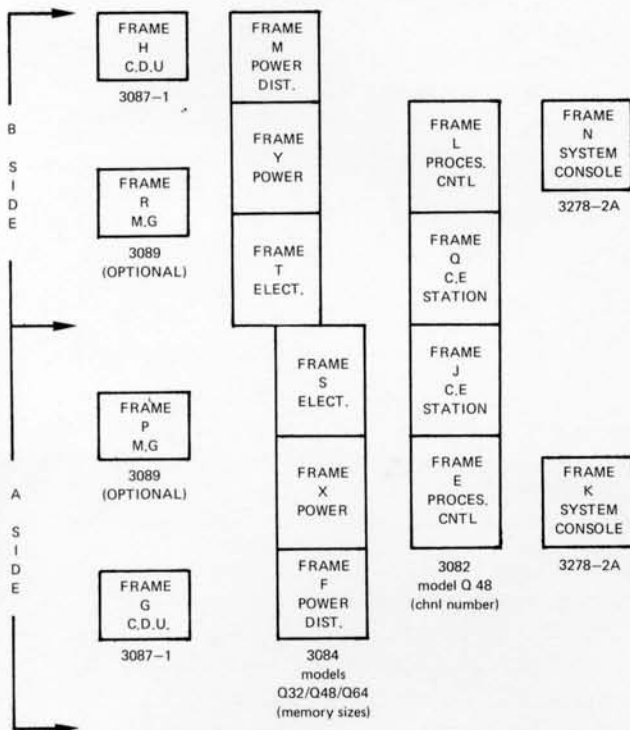


3084 Processor Complex

PRODUCT DESCRIPTION

The 3084 Processor Models Q is IBM'S largest processor for general purpose, high performance, large-scale multi-processing providing additional growth in the 308X family.

3084 PROCESSOR COMPLEX LAYOUT



General Characteristics

The 3084 Processor is switchable between 2 Configurations.

- The Single-Image Configuration
- Partitioned Configuration

A. SINGLE-IMAGE CONFIGURATION

The 3084 Model Q configured as Single-Image Configuration forms a tightly coupled multiprocessing system which runs under control of one single copy of XA-Control Program. It includes:

- 4 Central Processors
- 32, 48 or 64 Meg of Real Storage.
- 48 Channel Paths.
- Up to 4 Channel to Channel Adaptors.

B. PARTITIONED CONFIGURATION

The 3084 may be Partitioned into two independent Dyadic Processors. During this mode of operation, either or both sides may operate under System 370 Architecture or System 370-XA Architecture. Two System Control Programs will be used. This mode also allows for maintenance on one side concurrent with customer operation on the other side. Dynamic Switching to and from Partitioned Configuration is possible when using a System 370-XA System Control Program.

Installability / Upgradeability

The 3081 Model K is Field Upgradeable to a 3084 Model Q. A companion model change to the 3082 Processor Controller is required as well as the installation of an additional 3087 Coolant Distribution Unit, 3089 Power Unit (optional) and 3278-2A System Console.

Compatibility

The 3084, when in Partitioned Configuration, is fully compatible with S/370 or 370-XA SCP programs used on the 3081 or 3083 Processors. When in Single-Image Configuration, only /XA SCP may be used. Application programs that were supported on the 3081 and 3083 are compatible with the 3084. Any I/O supported on the 3081 or 3083 is supported on the 3084 in either Partitioned or Single-Image Configuration.

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